Dynamic Slack Reclamation with Procrastination Scheduling in Real-Time Embedded Systems

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Introduction

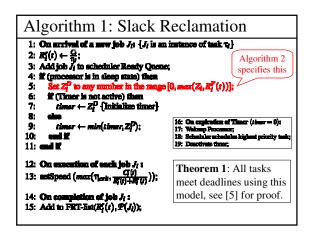
- Must reduce energy usage!
- · Two ways to save power
 - Slowdown (DVS)
 - Reduce dynamic power, increase execution time, static power unaffected
 Shutdown
 - Turn (almost) all power off for a given period of time
 - We can use *task procrastination* to glob together slack times
- Static power usage is growing
- This is due to increasing leakage current in newer & smaller processors, so slowdown isn't enough...
- Paper's goal:
 - Combine procrastination scheduling with dynamic slowdown techniques

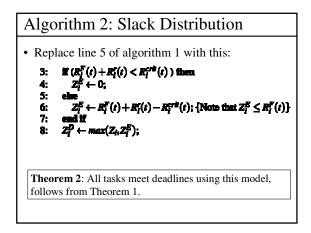
System Model

- Tasks τ_i of form {T_i, D_i, C_i}
 T_i = Period, D_i = Relative deadline, C_i = WCET
- Slowdown
 - $-\eta = DVS$ slowdown factor in [0,1]
 - $-\eta_i$ = Static slowdown of task i
 - η_{crit} = Slowdown with least energy per clock cycle
 The minimum value of η worth caring about
- Dynamic Slack Reclamation's two parts:
- Dynamic Stack Reclamation St – Slack Reclamation Algorithm
 - Generic mechanism for all procrastination/slowdown hybrids
 - Slack Distribution Policy
 - Specific policy to choose how much slack goes to procrastination versus slowdown

Variables & Structures Used

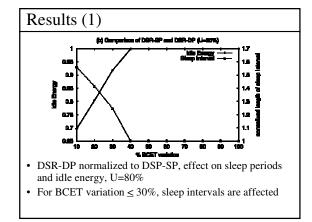
- J_i : current job of task τ_i
- $R_i^r(t)$: available run-time of J_i at time t
- $R_{i}^{F}(t)$: free time (slack) available to J_{i} at time t- Run-time from the FRT-list with priority $\geq P(J_{i})$
- $C_{i}^{r}(t)$: residual workload of job J_{i}
- $R^{crit}_{i}(t)$: run-time needed to finish J_i at speed η_{crit}
- Z_i : Statically derived procrastination delay
- Z^{D}_{i} : Dynamically derived procrastination delay
- *FRT-list* : Free Run Time List, a priority sorted list of available runtime from processes' slack

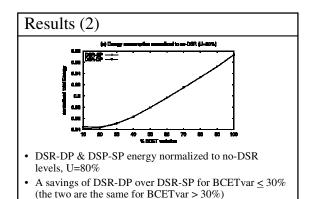


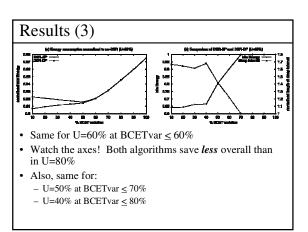


Experiment

- Three algorithms tested:
 - **no-DSR**: Static slowdown (η_i) with static procrastination intervals (Z_i)
 - **DSP-SP**: Dynamic slowdown (algorithm 1) with static procrastination (Z_i)
 - **DSP-DP**: Dynamic slowdown (algorithm 1) with dynamic procrastination (algorithm 2)







Some points

- When $U < \eta_{crit}$ (0.41 in the experiment):
 - "Static procrastination intervals dominate over dynamic slack available"
 - With nothing left to scavenge, DSR-DP does very little for these cases
- Overall, DSR-DP isn't a huge win over DSR-SP, because static procrastination already globs the majority of small idle times
- However, when these statically derived times are too short to shut down, the small boost given by DSR-DP could put them over the limit, and thus mean significant savings

Conclusion

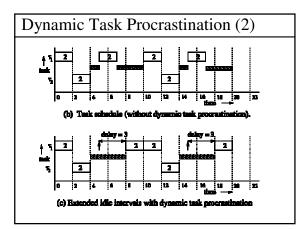
- Slowdown reduces dynamic power, but static power is becoming the problem in modern processors
- Shutting down allows us to cut off all power for a time
- Task procrastination works to lengthen idle times in which we can shut down
- The paper combines these two existing methods to get the best of each
- Idle energy savings of up to 70% are realized
- This savings will become more important as static power use increases in future chip designs

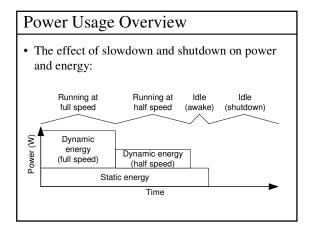
Any questions?

Additional reference slides follow ...

Dynamic Task Procrastination (1)

- On task completion, if no tasks left to execute: Shut down
- If a task arrives and we are shut down, then
- Find the max time Z_i^D that we can wait and still finish all tasks on time based on WCET
- Wake up the processor before the least Z_i^{D}
- Start processor and run EDF normally





Run-time Consumption

- A task τ_i consumes run-time in wall-clock seconds (i.e. η isn't involved in this calculation)
- If $R_{i}^{F}(t) > 0$, the run-time is taken from the *FRTlist*, else it uses its allotted run-time
- During idle periods, time is used from the *FRT*-*list* unless the list is empty
- These rules can be applied at job arrival & completion (rather than continuously)

